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Conventional Transistors And Beyond**

**By Samar K. Saha**

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[http://link.springer.com/chapter/10.1007/978-3-319-01165-3\\_1](http://link.springer.com/chapter/10.1007/978-3-319-01165-3_1)

Microelectronics Books. By David K. Ferry. Transistors and Interconnects at the Nanoscale. By Ashok Srivastava,  
<http://www.sponpress.com/books/subjects/SCEC133525/>

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transmission and applications in optoelectronic integrated circuit. Model to Circuit Design. Compact Modeling for Graphene FET Transistors.

<http://ieeexplore.ieee.org/xpl/topAccessedArticles.jsp?punumber=7729>

We study the overall progress in the field across all major ANN models, hardware design model onto a compact and reliable hardware Integrated Circuits

<http://www.sciencedirect.com/science/article/pii/S092523121000216X>

K. Jerry, "Model-Based Design and Beyond: The scheme is compared with the conventional circuit electrothermal design of nanoscale integrated circuits:

<http://dl.acm.org/citation.cfm?id=1118345>

Semiconductor device modeling creates models for the behavior of the electrical devices based on fundamental physics, such as the doping profiles of the devices. It

[http://en.wikipedia.org/wiki/Semiconductor\\_device\\_modeling](http://en.wikipedia.org/wiki/Semiconductor_device_modeling)

Compact models for integrated circuit design: conventional transistors and beyond (Link) CRC Press, a Taylor & Francis group July 2015. Book on SPICE modeling: 545

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<http://www.sciencedirect.com/science/article/pii/S0026271411001880>

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